

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	204009	(wafer\$1 or semiconductor\$1 or chip\$1 or substrat\$1 or IC or intergrated adj circuit or PCB or printed adj circuit adj board or PWB or printed adj wiring adj board)	USPAT	OR	ON	2005/12/16 13:54
L2	85614	L1 and(correct\$3 or error or inclinat\$3 or skew or slant or tilt\$3 or deviation)	USPAT	OR	ON	2005/12/16 13:41
L3	0	L2 and(turn\$3 or rotat\$3 or moving)with circumferential and(correct\$3 or error)with deviat\$3 and(CCD or camera or sensor\$1 or detector\$1)with serpentine and coordinat\$3	USPAT	OR	ON	2005/12/16 14:09
L4	18	L2 and(turn\$3 or rotat\$3 or moving)and(circumferential or circle )and(correct\$3 or error)with deviat\$3 and(CCD or camera or sensor\$1 or detector\$1)with(serpentine or sprial or circle)and coordinat\$3	USPAT	OR	ON	2005/12/16 13:44
L5	17	L4 and @ad<"20001121"	USPAT	OR	ON	2005/12/16 13:58
L6	683	correct\$3 and bump and block and (plurality or plural\$1)and (wafer\$1 or semiconductor\$1 or chip\$1 or substrat\$1 or IC or intergrated adj circuit or PCB or printed adj circuit adj board or PWB or printed adj wiring adj board)and(inclinat\$3 or shift\$3 or slopes or skew)	USPAT	OR	ON	2005/12/16 13:56
L7	23	L6 and(CCD or camera or sensor\$1 or detector\$1)and scann\$3 near3(serpentine or sprial or circle)	USPAT	OR	ON	2005/12/16 13:58
L8	18	L7 and @ad<"20001121"	USPAT	OR	ON	2005/12/16 13:58
L9	16	L8 and (detect\$3 or determine)and mark\$3	USPAT	OR	ON	2005/12/16 14:04
L10	0	L9 and(turn\$3 or rotat\$3 or moving)with circumferential and(correct\$3 or error)with deviat\$3 and(CCD or camera or sensor\$1 or detector\$1)with(sprial or serpentine or zig adj zag) and coordinat\$3	USPAT	OR	ON	2005/12/16 14:10
L11	0	L9 and(turn\$3 or rotat\$3 or moving)and(circumferential or circle) and(correct\$3 or error)with deviat\$3 and(CCD or camera or sensor\$1 or detector\$1)with(sprial or serpentine or zig adj zag) and coordinat\$3	USPAT	OR	ON	2005/12/16 14:11

L12	0	L9 and(turn\$3 or rotat\$3 or moving)and(circumferential or circle) and(correct\$3 or error)and deviat\$3 and(CCD or camera or sensor\$1 or déector\$1)and(sprial or serpentine or zig adj zag) and coordinat\$3	USPAT	OR	ON	2005/12/16 14:11
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	683	correct\$3 and bump and block and (plurality or plural\$1)and (wafer\$1 or semiconductor\$1 or chip\$1 or substrat\$1 or IC or intergrated adj circuit or PCB or printed adj circuit adj board or PWB or printed adj wiring adj board)and(inclinat\$3 or shift\$3 or slopes or skew)	USPAT	OR	ON	2005/12/16 14:26
L2	21	L1 and(CCD or camera or sensor\$1 or detector\$1)and scann\$3 near3(serpentine or sprial or circle)and(detect\$3 or determine)and mark\$3	USPAT	OR	ON	2005/12/16 14:20
L3	89327	382/145,151,181,286, "287", "289, 312".ccls.	USPAT	OR	ON	2005/12/16 14:27
L4	73	L3 and L1	USPAT	OR	ON	2005/12/16 14:27
L5	8	L4 and L2	USPAT	OR	ON	2005/12/16 14:28
L6	73	L1 and L3	USPAT	OR	ON	2005/12/16 14:28
L7	8	L2 and L6	USPAT	OR	ON	2005/12/16 14:39
L9	3701	438/14,460,612,613,614.ccls.	USPAT	OR	ON	2005/12/16 14:40
L10	5	L1 and L9	USPAT	OR	ON	2005/12/16 14:40
L11	0	L10 and L2	USPAT	OR	ON	2005/12/16 14:41
L12	967	257/e23.179.ccls.	USPAT	OR	ON	2005/12/16 14:41
L13	3	L1 and L12	USPAT	OR	ON	2005/12/16 14:41
L14	0	L13 and L2	USPAT	OR	ON	2005/12/16 14:42

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(correct\$3 and bump and block and (plurality or plural\$1)and (wafer\$1 or semiconductor\$1 or chip\$1 or substrat\$1 or IC or intergrated adj circuit or PCB or printed adj circuit adj board or PWB or printed adj wiring adj board)and(inclinat\$3 or shift\$3 or slopes or skew)and(CCD or camera or sensor\$1 or detector\$1)and scann\$3 near3(serpentine or sprial or circle)and(detect\$3 or determine)and mark\$3).clm.	US-PGPUB	OR	ON	2005/12/16 14:46

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1	INZZ	wafer OR semiconductor OR chip OR substrat OR IC OR intergrated ADJ circuit OR PCB OR printed ADJ circuit ADJ board OR PWB OR printed ADJ wiring ADJ board	unrestricted	691240	<a href="#">show titles</a>
2	INZZ	L1 AND correct* AND bump AND block AND (plurality OR plural*) AND (CCD OR camera OR sensor* OR detector*) AND scann* ADJ (serpentine OR sprial OR circle)	unrestricted	0	-
3	INZZ	L1 AND correct* AND bump AND block AND (plurality OR plural*) AND (inclinat* OR shift* OR slopes OR skew OR deviation)	unrestricted	0	-
4	INZZ	L1 AND correct* AND bump AND (inclinat* OR shift* OR slopes OR skew OR deviation)	unrestricted	0	-
5	INZZ	L1 AND (CCD OR camera OR sensor OR detector) AND scann* AND (serpentine OR sprial OR circle)	unrestricted	0	-
6	INZZ	L1 AND (CCD OR camera OR sensor OR detector) AND scann*	unrestricted	0	-
7	INZZ	L1 AND (CCD OR camera OR sensor OR detector) AND scanning	unrestricted	1	<a href="#">show titles</a>
8	INZZ	L1 AND L7	unrestricted	2	<a href="#">show titles</a>
9	INZZ	L8 AND (serpentine OR sprial OR circle)	unrestricted	1	<a href="#">show titles</a>

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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = NARITA

First Name = SHORIKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>08435616</u></a>	<a href="#"><u>5648729</u></a>	150	05/05/1995	BOARD POSITIONING METHOD AND APPARATUS OF THE METHODS	NARITA, SHORIKI
<a href="#"><u>08504654</u></a>	<a href="#"><u>5769236</u></a>	150	07/20/1995	COMPONENT COLLECTIVE AND COMPONENT COLLECTIVE FEEDING APPARATUS	NARITA, SHORIKI
<a href="#"><u>08825499</u></a>	<a href="#"><u>5955876</u></a>	150	03/31/1997	BOARD POSITIONING APPARATUS	NARITA, SHORIKI
<a href="#"><u>09335779</u></a>	<a href="#"><u>6302317</u></a>	150	06/18/1999	BUMP BONDING APPARATUS AND METHOD	NARITA, SHORIKI
<a href="#"><u>09423375</u></a>	<a href="#"><u>6098868</u></a>	150	11/04/1999	BUMP FORMING METHOD AND BUMP BONDER	NARITA, SHORIKI
<a href="#"><u>09690746</u></a>	<a href="#"><u>6329640</u></a>	150	10/18/2000	Heating apparatus for bump bonding, bump bonding method and bump forming apparatus, and semiconductor wafer	NARITA, SHORIKI
<a href="#"><u>09719768</u></a>	<a href="#"><u>6787391</u></a>	150	12/18/2000	METHOD OF FORMING BUMPS ON A WAFER UTILIZING A POST-HEATING	NARITA, SHORIKI
<a href="#"><u>09911801</u></a>	<a href="#"><u>6494358</u></a>	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	NARITA, SHORIKI
<a href="#"><u>09911802</u></a>	<a href="#"><u>6568580</u></a>	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	NARITA, SHORIKI
<a href="#"><u>09955110</u></a>	<a href="#"><u>6392202</u></a>	150	09/19/2001	HEATING APPARATUS FOR BUMP BONDING, BUMP BONDING METHOD AND BUMP FORMING APPARATUS, AND SEMICONDUCTOR WAFER	NARITA, SHORIKI
<a href="#"><u>09988704</u></a>	Not Issued	71	11/20/2001	Method and apparatus for correcting inclination of IC on semiconductor wafer	NARITA, SHORIKI

<u>10019700</u>	6818975	150	01/02/2002	ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE BUMP FORMING DEVICE, METHOD OF REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE DEVICE FOR REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE, AND ELECT	NARITA, SHORIKI
<u>10332026</u>	6910613	150	01/02/2003	DEVICE AND METHOD FOR FORMING BUMP	NARITA, SHORIKI
<u>10415587</u>	Not Issued	93	04/30/2003	BUMP FORMATION METHOD AND BUMP FORMING APPARATUS TO SEMICONDUCTOR WAFER	NARITA, SHORIKI
<u>10536361</u>	Not Issued	20	05/26/2005	Component-supplying head device, component-supplying device, component-mounting device, and method for moving mounting head portion	NARITA, SHORIKI
<u>10537406</u>	Not Issued	20	06/02/2005	Parts feeder	NARITA, SHORIKI
<u>10651103</u>	Not Issued	94	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	NARITA, SHORIKI
<u>10651199</u>	Not Issued	95	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR	NARITA, SHORIKI

				CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	
10761412	Not Issued	30	01/22/2004	Method of forming bumps on a wafer utilizing a post-heating operation, and an apparatus therefore	NARITA, SHORIKI
11114084	Not Issued	41	04/26/2005	Apparatus and method for forming bump	NARITA, SHORIKI

Inventor Search Completed: No Records to Display.

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Last Name

NARITA

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SHORIKI

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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = IKEYA

First Name = MASAHIKO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09423375</a>	6098868	150	11/04/1999	BUMP FORMING METHOD AND BUMP BONDER	IKEYA, MASAHIKO
<a href="#">09719768</a>	6787391	150	12/18/2000	METHOD OF FORMING BUMPS ON A WAFER UTILIZING A POST-HEATING	IKEYA, MASAHIKO
<a href="#">09988704</a>	Not Issued	71	11/20/2001	Method and apparatus for correcting inclination of IC on semiconductor wafer	IKEYA, MASAHIKO
<a href="#">10019700</a>	6818975	150	01/02/2002	ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE BUMP FORMING DEVICE, METHOD OF REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE DEVICE FOR REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE, AND ELECT	IKEYA, MASAHIKO
<a href="#">10234118</a>	Not Issued	93	09/05/2002	COMPONENT MOUNTING APPARATUS AND COMPONENT MOUNTING METHOD	IKEYA, MASAHIKO
<a href="#">10332026</a>	6910613	150	01/02/2003	DEVICE AND METHOD FOR FORMING BUMP	IKEYA, MASAHIKO
<a href="#">10415587</a>	Not Issued	93	04/30/2003	BUMP FORMATION METHOD AND BUMP FORMING APPARATUS TO SEMICONDUCTOR WAFER	IKEYA, MASAHIKO

<a href="#"><u>10651103</u></a>	Not Issued	94	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	IKEYA, MASAHIKO
<a href="#"><u>10651199</u></a>	Not Issued	95	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	IKEYA, MASAHIKO
<a href="#"><u>10761412</u></a>	Not Issued	30	01/22/2004	Method of forming bumps on a wafer utilizing a post-heating operation, and an apparatus therefore	IKEYA, MASAHIKO
<a href="#"><u>11114084</u></a>	Not Issued	41	04/26/2005	Apparatus and method for forming bump	IKEYA, MASAHIKO

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MASAHIKO

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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = TSUBOI

First Name = YASUTAKA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08678658</a>	5825914	150	07/11/1996	COMPONENT DETECTION METHOD	TSUBOI, YASUTAKA
<a href="#">09069203</a>	5872863	150	04/29/1998	COMPONENT DETECTION METHOD	TSUBOI, YASUTAKA
<a href="#">09622825</a>	6542238	150	10/12/2000	ELECTRONIC COMPONENT MOUNTING APPARATUS	TSUBOI, YASUTAKA
<a href="#">09988704</a>	Not Issued	71	11/20/2001	Method and apparatus for correcting inclination of IC on semiconductor wafer	TSUBOI, YASUTAKA
<a href="#">10019700</a>	6818975	150	01/02/2002	ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE BUMP FORMING DEVICE, METHOD OF REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE DEVICE FOR REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE, AND ELECT	TSUBOI, YASUTAKA
<a href="#">10234118</a>	Not Issued	93	09/05/2002	COMPONENT MOUNTING APPARATUS AND COMPONENT MOUNTING METHOD	TSUBOI, YASUTAKA
<a href="#">10415587</a>	Not Issued	93	04/30/2003	BUMP FORMATION METHOD AND BUMP FORMING APPARATUS TO SEMICONDUCTOR WAFER	TSUBOI, YASUTAKA
<a href="#">10651103</a>	Not Issued	94	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE	TSUBOI, YASUTAKA

				APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	
<u>10651199</u>	Not Issued	95	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	TSUBOI, YASUTAKA

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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = MAE

First Name = TAKAHARU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>06891028</u></a>	4763811	150	07/31/1986	PARTS FEEDER	MAE, TAKAHARU
<a href="#"><u>09335779</u></a>	6302317	150	06/18/1999	BUMP BONDING APPARATUS AND METHOD	MAE, TAKAHARU
<a href="#"><u>09423375</u></a>	6098868	150	11/04/1999	BUMP FORMING METHOD AND BUMP BONDER	MAE, TAKAHARU
<a href="#"><u>09690746</u></a>	6329640	150	10/18/2000	Heating apparatus for bump bonding, bump bonding method and bump forming apparatus, and semiconductor wafer	MAE, TAKAHARU
<a href="#"><u>09719768</u></a>	6787391	150	12/18/2000	METHOD OF FORMING BUMPS ON A WAFER UTILIZING A POST-HEATING	MAE, TAKAHARU
<a href="#"><u>09830508</u></a>	6619535	150	04/27/2001	WORKING METHOD FOR HOLDING A WORK OBJECT BY SUCTION	MAE, TAKAHARU
<a href="#"><u>09911801</u></a>	6494358	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	MAE, TAKAHARU
<a href="#"><u>09911802</u></a>	6568580	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	MAE, TAKAHARU
<a href="#"><u>09955110</u></a>	6392202	150	09/19/2001	HEATING APPARATUS FOR BUMP BONDING, BUMP BONDING METHOD AND BUMP FORMING APPARATUS, AND SEMICONDUCTOR WAFER	MAE, TAKAHARU
<a href="#"><u>09988704</u></a>	Not Issued	71	11/20/2001	Method and apparatus for correcting inclination of IC on semiconductor wafer	MAE, TAKAHARU
<a href="#"><u>10019700</u></a>	6818975	150	01/02/2002	ELECTRIC CHARGE GENERATING SEMICONDUCTOR	MAE, TAKAHARU

				SUBSTRATE BUMP FORMING DEVICE, METHOD OF REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE DEVICE FOR REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE, AND ELECT	
<u>10324001</u>	Not Issued	41	12/20/2002	Apparatus and method for mounting electronic components	MAE, TAKAHARU
<u>10332026</u>	6910613	150	01/02/2003	DEVICE AND METHOD FOR FORMING BUMP	MAE, TAKAHARU
<u>10415587</u>	Not Issued	93	04/30/2003	BUMP FORMATION METHOD AND BUMP FORMING APPARATUS TO SEMICONDUCTOR WAFER	MAE, TAKAHARU
<u>10508460</u>	Not Issued	20	10/18/2004	Electronic component mounting method and apparatus and ultrasonic bonding head	MAE, TAKAHARU
<u>10651103</u>	Not Issued	94	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE	MAE, TAKAHARU
<u>10651199</u>	Not Issued	95	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE	MAE, TAKAHARU

				REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	
10761412	Not Issued	30	01/22/2004	Method of forming bumps on a wafer utilizing a post-heating operation, and an apparatus therefore	MAE, TAKAHARU
11114084	Not Issued	41	04/26/2005	Apparatus and method for forming bump	MAE, TAKAHARU

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

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<input type="text" value="MAE"/>	<input type="text" value="TAKAHARU"/>
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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = KANAYAMA

First Name = SHINJI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>08098813</u></a>	5462626	150	07/29/1993	A METHOD OF BONDING AN LEAD AND A TOOL THERFOR	KANAYAMA, SHINJI
<a href="#"><u>08413441</u></a>	5854745	150	03/30/1995	METHOD AND APPARATUS FOR MOUNTING ELECTRONIC COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>08496691</u></a>	5582341	150	06/29/1995	BONDING APPARATUS FOR TERMINAL COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>08569937</u></a>	5894657	150	12/08/1995	MOUNTING APPARATUS FOR ELECTRONIC COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>08589005</u></a>	5783915	150	01/19/1996	LINEAR ACTUATING APPARATUS	KANAYAMA, SHINJI
<a href="#"><u>09029391</u></a>	6129203	150	02/27/1998	IC DISCARDING APPARATUS FOR FLIP CHIP MOUNTING FACILITY	KANAYAMA, SHINJI
<a href="#"><u>09119974</u></a>	6017812	150	07/21/1998	BUMP BONDING METHOD AND BUMP BONDING APPARATUS	KANAYAMA, SHINJI
<a href="#"><u>09122901</u></a>	6467158	150	07/27/1998	COMPONENT FEEDER	KANAYAMA, SHINJI
<a href="#"><u>09155720</u></a>	6055724	150	10/02/1998	METHOD AND DEVICE FOR SEALING IC CHIP	KANAYAMA, SHINJI
<a href="#"><u>09254687</u></a>	6332268	150	03/16/1999	METHOD AND APPARATUS FOR PACKAGING IC CHIP, AND TAPE-SHAPED CARRIER TO BE USED THEREFOR	KANAYAMA, SHINJI
<a href="#"><u>09266928</u></a>	6264704	150	03/12/1999	METHOD AND APPARATUS FOR MOUNTING COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>09335779</u></a>	6302317	150	06/18/1999	BUMP BONDING APPARATUS AND METHOD	KANAYAMA, SHINJI

<a href="#"><u>09354087</u></a>	6321973	150	07/15/1999	BUMP JOINING METHOD	KANAYAMA, SHINJI
<a href="#"><u>09373957</u></a>	6193136	150	08/13/1999	COMPONENT MOUNTING METHOD AND APPARATUS	KANAYAMA, SHINJI
<a href="#"><u>09388705</u></a>	Not Issued	161	09/02/1999	COMPONENT MOUNTING TOOL	KANAYAMA, SHINJI
<a href="#"><u>09446031</u></a>	6647616	150	12/15/1999	BUMP BONDING UNIT WITH TRAY STORAGE AND TRANSPORT APPARATUSES	KANAYAMA, SHINJI
<a href="#"><u>09690746</u></a>	6329640	150	10/18/2000	Heating apparatus for bump bonding, bump bonding method and bump forming apparatus, and semiconductor wafer	KANAYAMA, SHINJI
<a href="#"><u>09719768</u></a>	6787391	150	12/18/2000	METHOD OF FORMING BUMPS ON A WAFER UTILIZING A POST-HEATING	KANAYAMA, SHINJI
<a href="#"><u>09756307</u></a>	6467670	150	01/08/2001	METHOD AND APPARATUS FOR MOUNTING COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>09763959</u></a>	6439447	150	02/28/2001	BUMP JOINING JUDGING DEVICE AND METHOD, AND SEMICONDUCTOR COMPONENT PRODUCTION DEVICE AND METHOD	KANAYAMA, SHINJI
<a href="#"><u>09830508</u></a>	6619535	150	04/27/2001	WORKING METHOD FOR HOLDING A WORK OBJECT BY SUCTION	KANAYAMA, SHINJI
<a href="#"><u>09873347</u></a>	6506222	150	06/05/2001	METHOD AND APPARATUS FOR MOUNTING COMPONENT	KANAYAMA, SHINJI
<a href="#"><u>09911801</u></a>	6494358	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	KANAYAMA, SHINJI
<a href="#"><u>09911802</u></a>	6568580	150	07/25/2001	BUMP BONDING APPARATUS AND METHOD	KANAYAMA, SHINJI
<a href="#"><u>09955110</u></a>	6392202	150	09/19/2001	HEATING APPARATUS FOR BUMP BONDING, BUMP BONDING METHOD AND BUMP FORMING APPARATUS, AND SEMICONDUCTOR WAFER	KANAYAMA, SHINJI
<a href="#"><u>09964404</u></a>	6572005	150	09/28/2001	BUMP JOINING METHOD	KANAYAMA, SHINJI
<a href="#"><u>09988704</u></a>	Not	71	11/20/2001	Method and apparatus for	KANAYAMA,

	Issued			correcting inclination of IC on semiconductor wafer	SHINJI
<a href="#"><u>10019700</u></a>	6818975	150	01/02/2002	ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE BUMP FORMING DEVICE, METHOD OF REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE DEVICE FOR REMOVING ELECTRIC CHARGE FROM ELECTRIC CHARGE GENERATING SEMICONDUCTOR SUBSTRATE, AND ELECT	KANAYAMA, SHINJI
<a href="#"><u>10069401</u></a>	6830989	150	02/26/2002	METHOD AND APPARATUS FOR HANDLING ARRAYED PART	KANAYAMA, SHINJI
<a href="#"><u>10089184</u></a>	6692214	150	07/17/2002	PUSHER, PULLER LOADER, UNLOADER, AND WORKING DEVICE	KANAYAMA, SHINJI
<a href="#"><u>10089843</u></a>	6706130	150	04/04/2002	METHOD AND DEVICE FOR FRICTIONAL CONNECTION AND HOLDING TOOL USED FOR THE FRICTIONAL CONNECTION DEVICE	KANAYAMA, SHINJI
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<a href="#"><u>10419106</u></a>	Not Issued	161	04/21/2003	Bump joining method	KANAYAMA, SHINJI

<u>10508460</u>	Not Issued	20	10/18/2004	Electronic component mounting method and apparatus and ultrasonic bonding head	KANAYAMA, SHINJI
<u>10651103</u>	Not Issued	94	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	KANAYAMA, SHINJI
<u>10651199</u>	Not Issued	95	08/29/2003	BUMP FORMING APPARATUS FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVAL METHOD FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, CHARGE REMOVING UNIT FOR CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE, AND CHARGE APPEARANCE SEMICONDUCTOR SUBSTRATE	KANAYAMA, SHINJI
<u>10761412</u>	Not Issued	30	01/22/2004	Method of forming bumps on a wafer utilizing a post-heating operation, and an apparatus therefore	KANAYAMA, SHINJI
<u>10805407</u>	Not Issued	18	03/22/2004	Method and apparatus for handling arrayed components	KANAYAMA, SHINJI
<u>11114084</u>	Not Issued	41	04/26/2005	Apparatus and method for forming bump	KANAYAMA, SHINJI
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